

ABSTRACT OF THE DISCLOSURE

An arrangement for controlling the printing in a mail-processing device has a print data controller for pixel data editing while printing with a print head. The print data controller is composed of a pixel data-editing unit and a circuit including a DMA controller, an address generator and a printer controller. The DMA controller allows access to the binary pixel data stored in the pixel memory in order to make the data available to the pixel data-editing unit in data strings. The address generator generates addresses that are supplied to the pixel data-editing unit for the selection of the binary pixel data from an intermediately stored data string and for grouping in the required sequence. The printer controller drives the pixel data-editing unit in order to supply the binary pixel data in groups to a driver unit for the print head.

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